**Computer Architecture  
CSCE 3301-01  
Fall 2019  
MS1 Journal – Mohammed Abuelwafa  
October 28th, 2019  
For  
Dr. Cherif Salama**

* November 15th – 5:30 pm : 7:45 pm: Met with Abdelhakim and Marwan after reading the support files of the project to discuss it and discuss how we are going to work.
* November 18th – 12:00 pm : 8:00 pm: simulated the pipeline register without the single memory modification and checked its performance.
* November 19th – 3:00 pm : 11:00 am: Finished the final datapath module and with Abdelhakim and traced the results. Also, made the compression bonus and checked its performance.